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AUG 16 2004

TRANSMITTAL OF FORMAL DRAWINGS

Docket No.
BUR920040063US1 (17544)

In the Application Of: John M. Aitken, et al.

Application No.	Filing Date	Confirmation No.	Examiner	Customer No.	Group Art Unit
10/709,727	May 25, 2004	Unknown	Unknown	23389	Unknown

Invention: TRENCH TYPE BURIED ON-CHIP PRECISION PROGRAMMABLE RESISTOR

Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Transmitted herewith are:

4 sheets of formal drawing(s) for this application.

Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).



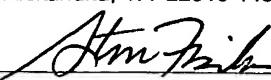
Signature

Steven Fischman
Registration No. 34,594

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Dated: August 13, 2004

I certify that this document and attached formal drawings are being deposited on August 13, 2004 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



Signature of Person Mailing Correspondence

Steven Fischman

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